

## MICROPHONE BIAS CIRCUIT

### ABSTRACT OF THE DISCLOSURE

5        A microphone bias circuit includes a first integrated circuit (IC) pin, a second IC pin, a first resistor, and a variable supply voltage buffer. The first resistor is operably coupled to the first IC pin and a return voltage. The second IC pin is operably coupled to receive analog signals from a microphone. The variable supply voltage buffer is operably coupled to produce a buffered supply voltage based on a variable impedance setting,  
10      wherein at least one off-chip component couples the second IC pin to the first IC pin and wherein the variable supply voltage buffer provides the buffered supply voltage to second IC pin as a microphone bias voltage.